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02 JUL 1997	7 16:21:48	U.S. Patent & Tra		P0011
US PAT NO:	5,486,864 [1	[MAGE AVAILABLE]	L7: 2 of 3	
one of sa a source means for storage means for	oid plurality of of clock pulses; measuring the t buffer in units incrementing, b	tiplexing means does input terminals; and transit time of signa of said clock pulses by the measured trans in transport packets	includes; l passed through sa: ; and it <b>time.</b> values con	id tained in
US PAT NO:	4,506,852 [1	MAGE AVAILABLE	L7: 3 of 3	
SUMMARY:				•
BSUM(10)				
simultaneou the time of comprising structure a launch vehi delayed	sly imparting bo deployment. Thi two or more cant nd payload struc cle. Release is the spin im	gement using a single oth linear and angulars single instrumentated springs compressed ture. The payload is achieved in a manner aparted to the payload oad during and after	momentum to the pality is a spring arm between launch vehreleasably secured obviating unbalance as the canted spri	yload at rangement nicle to the ed or
=> d his			_	
L1 L2 L3 L4 L5 L6 L7	SET PAGELEN SET LINELEN 87 S UNICAST? 23 S AGGREGA? 0 S (COMBIN? 231 S (COMBIN? 0 S L1 AND L4 1 S L4 (P) ME 3 S L4 (P) TI	IGTH 78  (P) PAYLOAD#\ OR COMPRESS?) (6A) AI OR COMPRESS?) (6A) PA  SSAGE# ME (P) INTERVAL#	PYLOAD#	
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